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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yi Ding

Assignee: ProMOS Technologies, Inc.

Title: Nonvolatile Memory Cells With Buried Channel Transistors

Application No.: 10/632,155 Filing Date: July 30, 2003

Examiner: Nhu, David Group Art Unit: 2818

Docket No.: M-15222 US

San Jose, California
November 3, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed except for United States Patents and United States Published Patent Applications.

Citation of these documents shall not be construed as:

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11-3-04 Michael Shenker 11-3-04
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Attorney for Applicant Date of Signature

Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.	Serial No.		
NOV 09 2004				M-15222 US	10/632,155		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant			
(Use several sheets if necessary)				Yi Ding			
				Filing Date	Group		
				July 30, 2003	2818		
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,265,739	Jul. 2001	Yaegashi et al.			
	AB	6,747,310	Jun. 2004	Fan et al.			
	AC	2003/0205776	Nov. 2003	Yaegashi et al.			
	AD	6,468,865	Oct. 2002	Yang et al.			
	AE	6,218,689	Apr. 2001	Chang et al.			
	AF	6,214,669	Apr. 2001	Hisamune			
	AG	6,162,682	Dec. 2000	Kleine			
	AH	6,232,185	May-01	Wang			
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	AJ	6,436,764	Aug. 2002	Hsieh			
	AK	6,344,993	5 Feb. 2002	Harari et al.			
	AL	5,668,757	Sept. 1997	Jeng			
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	AP						
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	AX						
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<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>							